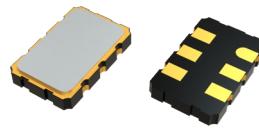


# 5.0 x 3.2mm SMD LVPECL/LVDS Voltage Controlled Crystal Oscillator

## Feature

- Typical 5.0 x 3.2 x 1.25 mm 6 pads ceramic SMD package.
- Tight symmetry (45 to 55%) available.
- Tri-state enable/disable

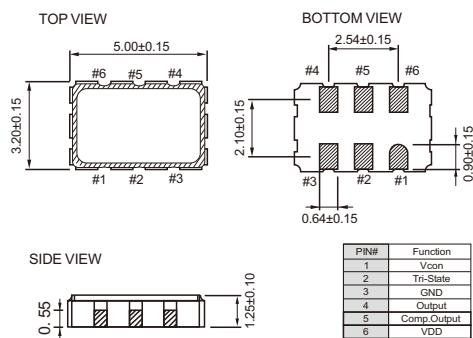


## Electrical Specifications

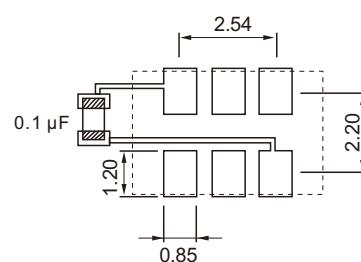
Parameter	LVPECL 3.3V		LVDS 3.3V		Unit
	Min.	Max.	Min.	Max.	
Supply Voltage Variation(VDD)	VDD-5%	VDD+5%	VDD-5%	VDD+5%	V
Frequency Range	30	250	30	250	MHz
Standard Frequency	122.88,153.6,155.52,156.25				MHz
Absolute Pulling Range (APR)	±50		±50	-	ppm
Control Voltage Range	0.3	3.0	0.3	3.0	V
Supply Current 30MHz Fo 250 MHz			100	75	mA
Output Level	Output High (logic "1")	2.275	-	1.6	V
	Output Low (logic "0")		1.68	0.9	
Transition Time: Rise/Fall Time +			1.0	1.0	nSec
Start Time			3	3	mSec
Tri-State (input to Pin 2)	Enable (High voltage or floating)	2.31		2.31	V
	Disable (Low voltage or GND)		0.99	0.99	
Linearity			10	10	%
Modulation Bandwidth (BW)	20		20		kHz
Input Impedance			1	1	MΩ
RMS Phase Jitter	F0 < 100 MHz			1.0	pSec
	100 MHz ≤ F0 < 125 MHz			0.7	
	125 MHz ≤ F0 < 150 MHz			0.5	
	150 MHz ≤ F0 < 250 MHz			0.3	
Phase Noise @122.88MHz	100 Hz			-105	dBc/Hz
	1KHz			-128	
	10KHz			-145	
Aging (@ 25°C 1st year)			±3	±3	ppm
Storage Temp. Range	-55	125	-55	125	°C

Standard frequencies are frequencies which the crystal has been designed and does not imply a stock position  
+ Transition times are measured between 20% and 80% of VDD.

## Dimension(mm)



## Solder Pad Layout(mm)



## FREQ. STABILITY vs. TEMP. RANGE

Temp. (°C)	ppm	±25	±50
-10 ~ +60	O	O	
-20 ~ +70	O	O	
-40 ~ +85	X	O	

O: Available Δ:Conditional X: Not available

Inclusive of calibration @ 25 °C, operating temperature range, input voltage variation, load variation, aging (1 st year), shock, and vibration