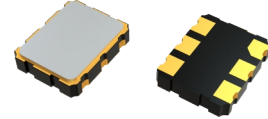


7.0 × 5.0 mm Ultra Low Phase Jitter LVPECL SMD Crystal Oscillator

Feature

- Typical 7.0 × 5.0 × 1.45 mm 6 pads SMD package
- Ultra low jitter performance: < 100 fs RMS from 12KHz-20MHz
- Tight symmetry (45 to 55%) available
- Complementary output



Electrical Specifications

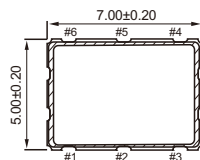
Parameter		LVPECL				Unit
		3.3V		2.5V		
		Min.	Max.	Min.	Max.	
Supply Voltage Variation		3.135	3.465	2.375	2.625	V
Frequency Range		70	170	100	160	MHz
Standard Frequency		100,125,155.52,156.25				MHz
Supply Current		-	75	-	75	mA
Transition Time :Rise/Fall Time		-	1	-	1	nSec
Output Level	Output High	2.275		1.475		V
	Output Low		1.68		1.095	
Start Time		-	10	-	10	mSec
Tri-State (Input to Pin 1 or Pin 2)	Enable	2.31	-	1.75	-	V
	Disable	-	0.99	-	0.75	
RMS Phase Jitter (Integrated12KHz to 20MHz)		-	0.1	-	0.1	pSec
Phase Noise @156.25MHz	100Hz offset	-100		-100		dBc/Hz
	1KHz offset	-130		-130		dBc/Hz
	10KHz offset	-150		-150		dBc/Hz
Aging(@25°C 1st year)		-	±3	-	±3	ppm
Storage Temp. Range		-55	125	-55	125	°C

Standard frequencies are frequencies which the crystal has been designed and does not imply a stock position

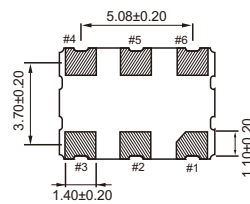
.+ Transition times are measured between 20% and 80% of VDD,

Dimension(mm)

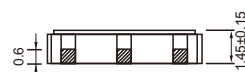
TOP VIEW



BOTTOM VIEW

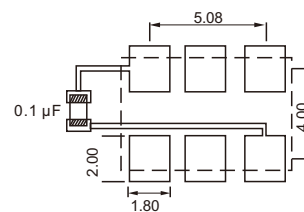


SIDE VIEW



PIN#	Function
1	NC/Tri-State
2	Tri-State/NC
3	GND
4	Output
5	Comp Output
6	VDD

Solder Pad Layout(mm)



To ensure optimal oscillator performance, place a by-pass capacitor of 0.1 µF as close to the part as possible between Vdd and GND pads.

FREQ. STABILITY vs. TEMP. RANGE

Temp. (°C) \ ppm	±25	±50
-10 ~ +60	Δ	○
-20 ~ +70	Δ	○
-40 ~ +85	x	○

○: Available Δ: Conditional X: Not available

Inclusive of calibration @ 25 °C, operating temperature range, input voltage variation, load variation, aging (1st year), shock, and vibration