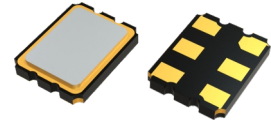


3.2 × 2.5mm SMD Differential Output Oscillator

Feature

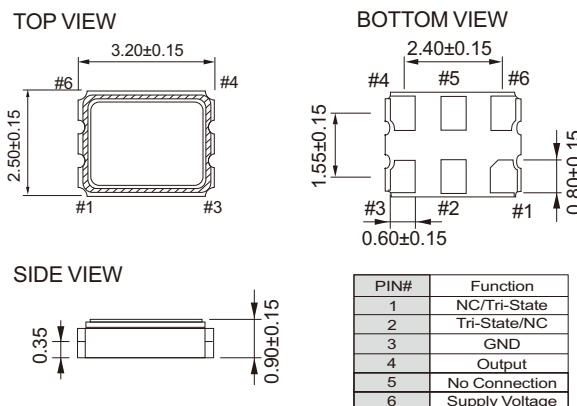
- Low power supply voltage: 3.3, 2.5 supply options
- Differential output : LVPECL, LVDS
- Frequency support from 10MHz to 1.5GHz
- Low noise typical: 0.8 ps at 12kHz to 20MHz frequency offsets
- Temperature range: -40 to 85 °C operation
- Fast delivery



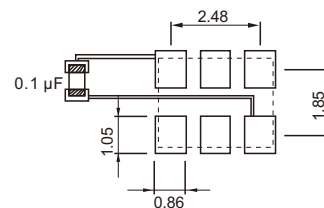
Electrical Specifications

Parameter		LVPECL				LVDS				Unit
		3.3V		2.5V		3.3V		2.5V		
		Min.	Max.	Min.	Max.	Min.	Max.	Min.	Max.	
Supply Voltage Variation		3.135	3.465	2.375	2.625	3.135	3.465	2.375	2.625	V
Frequency Range		10	1500	10	1500	10	1500	10	1500	MHz
Supply Current		-	50	-	50	-	50	-	50	mA
Output level	Output High	2.275	2.7	1.475	1.9	-	1.6	-	1.6	V
	Output Low	1.45	1.68	0.65	0.88	0.9	-	0.9	-	
Transition Time : Rise/Fall Time		-	1.0	-	1.0	-	1.0	-	1.0	nSec
Start Time		-	10	-	10	-	10	-	10	mSec
Tri-State (Input to Pin 1/2)	Enable	2.31	-	1.75	-	2.31	-	1.75	-	V
	Disable	-	0.99	-	0.75	-	0.99	-	0.75	
RMS Phase Jitter(integrated 12KHz ~ 20MHz)			1.5	-	1.5	-	1.5	-	1.5	pSec
Phase Noise @250MHz VDD=3.3v	1KHz	-107		-107		-107		-107		dBc/Hz
	10KHz	-111		-111		-111		-111		
	100KHz	-114		-114		-114		-114		
	1MHz	-125		-125		-125		-125		
	20MHz	-147		-147		-147		-147		
Aging(@25 1st year)		-	±3	-	±3	-	±3	-	±3	ppm
Storage Temp. Range		-55	125	-55	125	-55	125	-55	125	°C

Dimension(mm)



Solder Pad Layout(mm)



To ensure optimal oscillator performance, place a by-pass capacitor of 0.1 μF as close to the part as possible between Vdd and GND pads.

FREQ. STABILITY vs. TEMP. RANGE

Temp. (°C) \ ppm	±25	±50
-10 ~ +60	○	○
-20 ~ +70	○	○
-40 ~ +85	Δ	○

○: Available Δ: Conditional X: Not available

Inclusive of calibration @ 25 °C, operating temperature range, input voltage variation, load variation, aging (1st year), shock, and vibration