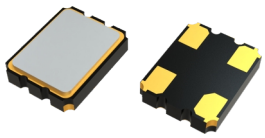
3.2 ×2.5 mm Extended Operating Temperature Range SMD Crystal Oscillator

Feature

- Typical 3.2 x 2.5 x 0.95 mm SMD package.
- Extended Industrial Operating Temperature Range -55~+125°C
- Low jitter and phase noise(25ps Pk-Pk Period jitter,typical)
- Tight symmetry（45 to 55%) available.
- Operation voltage : 1 .8V, 2.5V, 3.3V.
- Tri-state enable/disable.

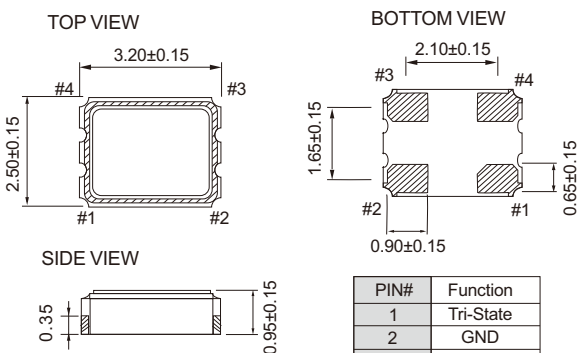


Electrical Specifications

Parameter		3.3V		2.5V		1.8V		Unit
		Min.	Max.	Min.	Max.	Min.	Max.	
Supply Voltage Variation		2.97	3.63	2.25	2.75	1.62	1.98	V
Frequency Range		1.25	100	1.25	100	1.25	100	MHz
Supply Current	FO<80MHz	-	10	-	8	-	5	mA
	80 MHz≦FO	-	15	-	10	-	8	mA
Duty Cycle		45	55	45	55	45	55	%
Transition Time :Rise/Fall Time		-	3	-	4	-	5	nSec
Output Level (CMOS)	Output High(Logic"1")	2.97		2.25		1.62		V
	Output Low(Logic"0")		0.33		0.25		0.18	
Start Time		-	2	-	2	-	2	mSec
Tri-State (Input to Pin 1)	Enable(High Voltage or floating)	2.31	-	1.75	-	1.26	-	V
	Disable(Low Voltage or GND)	-	0.99	-	0.75	-	0.54	
Period Jitter (Pk-Pk)		-	40	-	40	-	40	pSec
RMS Phase Jitter (integrated12KHz to 20MHz)		-	1	-	1	-	1	pSec
Aging(@25 1st year)		-	±3	-	±3	-	±3	ppm
Storage Temp. Range		-55	125	-55	125	-55	125	°C

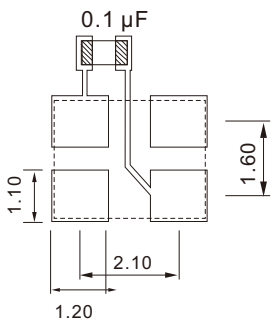
Standard frequencies are frequencies which the crystal has been designed and does not imply a stock position
.+ Transition times are measured between 10% and 90% of VDD, with an output load of 15pF.

Dimension(mm)



PIN#	Function
1	Tri-State
2	GND
3	Output
4	VDD

Solder Pad Layout(mm)



To ensure optimal oscillator performance, place a by-pass capacitor of 0.1 µF as close to the part as possible between Vdd and GND pads.

FREQ. STABILITY vs. TEMP. RANGE

ppm / Temp. (°C)	±30	±40	±50	±100
-40 ~ +85	○	○	○	○
-40 ~ +105	△	○	○	○
-40 ~ +125	X	△	○	○
-55 ~ +125	X	X	△	○

○: Available △:Conditional X: Not available
Inclusive of calibration @ 25 °C, operating temperature range, input voltage variation, load variation, aging (1st year), shock, and vibration